IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Atty. Docket: 02-GR2-175

Lionel GRILLO

Group Art Unit: 2817

Serial No. 10/649,998

Confirmation No. 6062

Filed: August 26, 2003

For: VARIABLE GAIN LOW-PASS FILTER

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

> This IDS is submitted under: _XX 37 CFR 1.97(b) - No Fee.

> > 37 CFR 1.97(c) - Fee.

SIR:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that other art than that identified exists; nor that the information provided is prior art; nor that a search has been made.

	The Commissioner is authorized to cha	arge any required fees under 37 CFR 1.17(p) and (i) (1) to
Depos	it Account No. 50-1556.	
Date: _	1/26/04	Respectfully submitted, By: Jon A. Gibbons Registration No. 37,333

FLEIT, KAIN, GIBBONS, GUTMAN, BONGINI & BIANCO P.L.

____ 37 CFR 1.97(c) - No Fee, with Certification.

37 CFR 1.97(d) - Fee, Certification & Petition.

551 NW 77th Street

Suite 111

Boca Raton, Florida 33487 Telephone: (561) 989-9811 Facsimile: (561) 989-9812

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service by first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Name of Person Mailing Paper

Signature of Person Mailing Paper

Date of Deposit

Form PTO-U.S. Dept. of Commerce Patent & Trademark Office AN 3 0 2004 List of Documents Cred by Applicant (Use Several sheets if necessary)

Atty. Docket: 02-GR2-175

Applicant: Lionel GRILLO

Filing Date: August 26, 2003

Group: 2817

Serial No. 10/649,998

U.S. PATENT DOCUMENTS										
Ex'rs In'l		Document Number	Date	Name	Class	Sub- class	Filing Date, if applicable			
		·								
					ļ					
			ļ		ļ	<u> </u>				
					<u> </u>					
					<u> </u>					
					<u> </u>					
		FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub- class	Transl'n Yes/No			
				11 11 11						
		1								
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)										
	AA1	P.R. Gray e	P.R. Gray et al. "Analysis and Design of Analog Integrated Circuits", March 2001, New York, XP002241157, pp. 830 & 832.							
	AA2		Menolfi C. et al. "A Low-Noise CMOS Instrumentation Amplifier for Thermoelectric Infrared Detectors", IEEE Journal of Solid-State Circuits, IEEE, Inc. New York, Vol. 32, No. , July 1997, pp. 968-976, XP000729356.							
	AA3	Filter and V	Gopinathan V. et al. "Design Considerations and Implementation of a Programmable High-Frequency Continuous-Time Filter and Variable-Gain Amplifier in Submicrometer CMOS", IEEE Journal of Solid State circuits, IEEE Inc., New York, Vol. 34, No. 12, December 1999, pp. 1698-1707, XP000932500.							
	AA4		Daubert S. J. et al. "A Transistor-Only Current-Mode Sigma Delta Modulator", IEEE Journal of Solid State Circuits, IEEE Inc., New York, Vol. 27, No. 5, May 1992, pp. 821-830, XP000291092.							
	AA5	French Pre	French Preliminary Search Report dated May 14, 2003 for French Application No. 0210751.							
Examiner: Date Considered:										

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.